LTSPICE BASIC TUTORIAL

Index:

- I. Opening LTSpice
- II. Drawing the circuit
 - A. <u>Making Sure You Have a GND</u> B. <u>Getting the Parts</u> C. <u>Placing the Parts</u>

 - D. Connecting the Circuit
 - E. <u>Changing the Name of the Part</u>
 F. <u>Changing the Value of the Part</u>

 - G. Using Net Labels
 - H. <u>Saving</u> I. <u>Printing</u>

III. Simulation

- A. <u>Before you do the simulation</u>
 B. <u>Choosing a simulation</u>

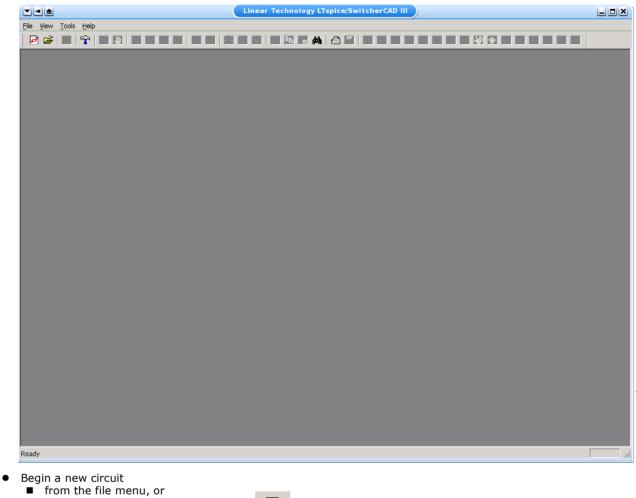
- C. <u>Graphing</u> D. <u>Adding/Deleting Traces</u> E. Doing Math
- F. Labelling
- G. Finding Points
- H. <u>Saving</u> I. <u>Printing</u>
- IV. <u>Simulation Commands</u> A. <u>DC operating point</u> B. <u>Transient</u> C. <u>AC Analysis</u> D. <u>DC Sweep</u> E. <u>Noise</u> F. <u>Other types of analysis</u>
- V. Types of Sources
 - A. Voltage Sources
 - 1. <u>DC</u> 2. <u>SINE</u>

 - 3. <u>PULSE</u> 4. <u>EXP</u> 5. <u>SFFM</u>

 - 6. <u>PWL</u> 7. <u>PWL File</u>
 - B. Current Sources

I. Opening LTSpice:

• Find LTSpice on the C-Drive. The opening screen will look like this:



from the menu, or
 click on the "New Schematic" icon.
 Now you will see this:



	Linear Technology LTspice/SwitcherCAD III - [Draft1.asc]	_ 🗆 🗙
File Edit Hierarchy View Simulate Tools Window		L B X
	/■ ■ ■ ■ ▲ ▲ △ ● ℓ → 	
,		

II. Drawing the circuit:

- A. Adding a GND: This is very important. You cannot do any simulation on the circuit if you don't have a ground. To place a ground, you can
 - press the 'g' key, or
 - use the ground icon,
 get it from the 'Edit' menu.

If you aren't sure where to put it, place it near the bottom of the drawing. B. Getting the other Parts:

- - The next thing that you have to do is get some or all of the parts you need.
 This can be done by
 - - clicking on the icon for a specific component;
 - < ≠ 3 文

(This is good for common components such as resistors, capacitors, etc.)

- clicking on the 'component' button;
 pressing "F2"; or
- going to "Edit" and selecting "Component..."
- Going to fail selecting component...
 Once this box is open, select a part that you want in your circuit. This can be done by typing in the name or scrolling down the list until you find it.

THA (Select Compo	nent Symbol 🗙
Top Directory: C:\Pro	gram Files\LTC\SwC4	ADIII\lib\sym
C\Program Files\LT	C\SwC4DIII\ib\sum\	Upen this macromodel's test fixture
[Comparators] [Digital] [FilterProducts] [Misc] [Dptos] [PowerProducts] [References] [SpecialFunctions] bi 2	bv cap CNSW csw current diode e e 2 f FerriteBead FerriteBead2	FerriteBead_Z(I) 9 92 h ind ind2 LED load load2 load2 lonp Itline
Cancel		 ОК

- Some common parts are:
 - res resistor
 - cap capacitor
 - ind inductor
 - diode diode
 - voltage any kind of power supply or battery
 - Anything in [] is a library, which contains many parts.
- To rotate parts so that they will fit in your circuit nicely, press "Ctrl+R" before placing the part. If you want to reflect (or 'Mirror') the
 part, press "Ctrl+E".
- Upon selecting your parts, click where you want them placed (somewhere on the grey page with the dots). Don't worry about putting it in exactly the right place, it can always be moved later.
- Each type of part can be placed multiple times in succession, and they will be automatically numbered. when you want to stop placing a particular type of part, right-click or press 'Esc'.

C. Placing the Parts

- You should have most of the parts that you need at this point.
- Now, all you do is put them in the places that make the most sense (usually a rectangle works well for simple circuits). To move parts,
 - click on the 'move' icon, 🖄 and then you may select parts and drag them where you want them.
 - (When you have a part selected for a move, you can rotate or reflect it as well.)
- If you have any parts left over, just select them and press "Delete".

D. Connecting the Circuit:

- Now that your parts are arranged well, you'll have to attach them with wires.
- Go up to the tool bar and
 - select the "Draw Wire" button
 - "F3" or
 - go to "Edit" and select "Draw Wire".
- With the pencil looking pointer, click on one end of a part, when you move your mouse around, you should see crossed lines appear. Attach the other end of your wire to the next part in the circuit.
- Repeat this until your circuit is completely wired.
- If you want to make a node (to make a wire go more then one place), click somewhere on the wire and then click to the part (or the other wire). Or you can go from the part to the wire. You should see a square block when 3 or more wires connect at a point.
- To get rid of the pencil, right click.
- E. Changing the Name of the Part:
 - You probably don't want to keep the names C1, C2 etc., especially if you didn't put the parts in the most logical order. To change the name, right click on the present name (C1, or R1 or whatever your part is), then a box will pop up (Enter New Reference Designator), where you can type in the name you want the part to have.

THA(Enter new refe	rence designator for 🛛 🗙
Bottom	ification al Text	OK Cancel
C4		

• Please note that if you double click on the part or its value, no box will appear.

F. Changing the Value of the Part:

• If you only want to change the value of the part (if you don't want all your resistors to be 1K ohms), you can right click on the part, (*not* the name), and a box title by the part name (such as "Resistor") will appear. The number of fields in the box will depend on the type of part it is. Type in the new value and press OK. Use u for micro as in uF = microFarad.

Capacitor - C4
Manufacturer: ······· Part Number: ······· Type: ······ Cancel
Select Capacitor
Capacitor Properties Capacitance(F):
Voltage Rating[V]:
RMS Current Rating[4]:
Equiv. Series Inductance[H]:
Equiv. Parallel Resistance[o
Equiv. Parallel Capacitance[F]:
Mean Time Between Failures[hr]:
Parts Per Package:

G. Using Net Labels:

- These are important if you want to user your own identifiers for points in the network where you want to determine voltages rather than having to work with the node numbers that LTSpice assigns.
- To add net labels,
 press "F4", or
- click on the "Label Net" icon
 select "Edit/Label Net" from the menu.
 When you do this, a window will pop up where you assign the label you want to use for the net.

▼ া 🔺 🛛 Net Name 🗙
C GND(global node 0)
Port Type: None 💌
(Port type is only visible if drawn at the end of a wire.)
Cancel

H. Saving:

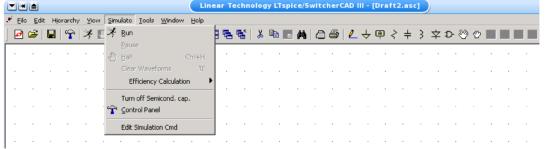
• To save the circuit, use the save button 🗳 on the tool bar or any other method you would normally use to save files. I. Printing:

as usual. • To print, you may use the menu or the print icon

III. Simulation:

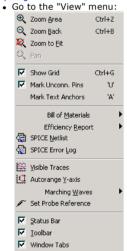
- A. Before you do the simulation:
 - You have to have your circuit properly drawn and saved.
 - There must not be any floating parts on your page (i.e. unattached devices).
 You should make sure that all parts have the values that you want.

 - There are no extra wires.
 - It is essential that you have a ground in your circuit.
- B. Choosing a simulation:
 - Click on the Simulate button on the tool bar 🎽 or use the "Simulate/Edit Simulation Cmd" command. r Technology LTspice/SwitcherCAD III - [Draft2.asc]



- Enable whatever type(s) of anlysis you want using the Edit Simulation Command window. The last one you choose is the one which will be done when you simulate.
- Click on the Simulate button on the tool bar ${}^{\mathbb{Z}}$ or use the "Simulate/Run" command.
- It will check to make sure you don't have any errors. If you do have errors, correct them.

C. Graphing:



- D. Adding/Deleting Traces:
 - Use "Visible Traces" or 🖺 on the toolbar to select all the traces you want.
 - The add traces window allows you to choose varaious signals from the circuit, or to create mathematical expressions involving them.

<pre>Image: Image: Imag</pre>	Add Traces to plot	×
Available data: V(a) V(b) IIC1) IIR1) IIR2) IIV1) frequency	Only list traces containing	OK Cancel
Expression(s) to add:		
🔽 AutoRange		

- To delete a trace, select its title on the graph and press "Delete".
- E. Doing Math:
 - In Visible Traces, there are functions that can be performed, these will add/subtract (or whatever you chose) the lines together.
 Select Visible Waveforms

Select Waveforms to Plot: Ctrl-Click to toggle Alt-Double-Click to enter an expression	OK Cancel
V(a) V(b) I(C1) I(R1) I(R2)	
liv1)	
Auto Range	

- Select the signal(s) that you wish to have displayed.
- There are many functions here that may or may not be useful. If you want to know how to use them, you can use LTSpice's Help Menu.
- F. Labelling:
 - Click on Text Label Aa on top tool bar.
 - Type in what you want to write.
 - Click OK
 - You can move this around by single clicking and dragging.
- G. Finding Points:
 - Click on the name of the trace you want to look at and then a cursor window will appear, showing information about the point

	RC.r	aw	×
Cursor 1	V(n001)		
Freq: 125.893Hz	Mag:	0dB	6
	Phase:	0.397569f*	[c
	Group Delay:	6.07908e-005fs	C
Cursor 2			
Freq: N/A	Mag:	N/A] (
	Phase:	N/A	[]
	Group Delay:	N/A	
	Ratio	(Cursor2 / Cursor1)	٦
Freq: - N/A-	Mag:	N/A	1
	Phase:	N/A	1
	Group Delay:	N/A	1

currently selected. 🔚

• You can use the cursor keys to move back and forth through the data points.

H. Saving:

• To save your probe you need to go into the tools menu and click display, this will open up a menu which will allow you to name the probe file and choose where to save it. You can also open previously saved plots from here as well.

- I. Printing:
 - Select Print in Edit or on the toolbar $\stackrel{ heta}{=}$.
 - Print as usual.

- IV. Simulation Commands
 A. DC Operating Point

 Transient AC Analysis DC sweep Noise DC Transfer DC op pnt

 Compute the DC operating capacitances as open circuits and
 inductances as short circuits.

 Syntax: .op

 .op
 - This is a simple, but incredibly useful analysis. It will not give you anything to plot, but it will indicate the voltages at all nodes and currents through all devices in the circuit.

Cancel

ОК

B. Transient

THA .	Edit Simulatio	on Command	×
Transient AC Analysis	DC sweep Noise	DC Transfer DC op pnt	
Perfo	rm a non-linear, time-d	omain simulation.	
	Stop Tim	ie:	
Ti	me to Start Saving Dat	ta:	
	Maximum Timeste	ep:	
Start external DC	supply voltages at OV	e 🗖	
Stop simulating if s	teady state is detected	d: 🗖	
Don't reset T=0 when s	teady state is detected	d: 🗖	
Step	the load current source	e: 🗖	
Skip Initial	operating point solution	n: 🗖	
Syntax: .tran <tstop> [<op < th=""><th>ion> [<option>]]</option></th><td></td><td></td></op <></tstop>	ion> [<option>]]</option>		
Cancel	OK		

- The transient analysis is probably the most important analysis you can run in LTSpice, and it computes various values of your circuit over time. Two very important parameters in the transient analysis are:
 - Stop Time.
 - Time to Start Saving Data
 - Maximum Timestep
 - (various checkboxes....later)
- The ratio of **Stop Time: Maximum Timestep** determines how many calculations LTSpice must make to plot a wave form. LTSpice always defaults the start time to zero seconds and going until it reaches the user defined final time. It is incredibly important that you think about what timestep you should use before running the simulation, if you make the timestep too small the probe screen will be cluttered with unnecessary points making it hard to read, and taking extreme amounts of time for LTSpice to calculate. However, at the opposite side of that coin is the problem that if you set the timestep too high you might miss important phenomenon that are occurring over very short periods of time in the circuit. Therefore play with step time to see what works best for your circuit.
- You can set a step ceiling which will limit the size of each interval, thus increasing calculation speed. Another handy feature is the
 Fourier analysis, which allows you to specify your fundamental frequency and the number of harmonics you wish to see on the plot.
 LTSpice defaults to the 9th harmonic unless you specify otherwise, but this still will allow you to decompose a square wave to see it's
 components with sufficient detail.

C	۸C	Ana	lycic
C.	AC	Alla	17515

- HA	Edit Simulation Command	×
Transient AC Analysis	DC sweep Noise DC Transfer DC op pnt	
Compute the small signal	AC behavior of the circuit linearized about its DC opera point.	iting
Number o	Type of Sweep: Octave	
Syntax: .ac <oct, dec,="" lin=""></oct,>	<npoints> <startfreq> <endfreq></endfreq></startfreq></npoints>	
Cancel	ŌK	

The AC analysis allows you to plot magnitude and/or phase versus frequency for different inputs in your circuit.

- Type of Sweep
 - In the AC anlysis menu you have the choice of three types of analysis:
 - Linear,
 - Octave and
 - Decade.

These three choices describe the X-axis scaling which will be produced in probe. For example, if you choose decade then a sample of your X-axis might be 10Hz, 1kHz, 100kHz, 10MHz, etc.... Therefore if you want to see how your circuit reacts over a very large range of frequencies choose the decade option.

- You now have to specify at how many points you want LTSpice to calculate frequencies, and what the start and end frequency will be. That is, over what range of frequencies do you want to simulate your circuit.
 - Number of points
 - Start Frequency
 - Stop Frequency

D. DC Sweep

- The DC sweep allows you to do various different sweeps of your circuit to see how it responds to various conditions.
- For all the possible sweeps,
 - voltage,
 - current,
 - temperature, and
 - parameter and global
- you need to specify a start value, an end value, and the number of points you wish to calculate.
- For example you can sweep your circuit over a voltage range from 0 to 12 volts. The main two sweeps that will be most important to us at this stage are the voltage sweep and the current sweep. For these two, you need to indicate to LTSpice what component you wish to sweep, for example V1 or V2.
- Another excellent feature of the DC sweep in LTSpice, is the ability to do a **nested sweep**.
- A nested sweep allows you to run two simultaneous sweeps to see how changes in two different DC sources will affect your circuit.
 Once you've filled in the main sweep menu, click on the nested sweep button and choose the second type of source to sweep and name it, also specifying the start and end values. (Note: In some versions of LTSpice you need to click on enable nested sweep). Again you can choose Linear, Octave or Decade, but also you can indicate your own list of values, example: 1V 10V 20V. DO NOT separate the values with commas.
- E. Noise
 - LTSpice will simulate noise for you either on the output or the input of the circuit. These noise calculations are performed at each frequency step and can be plotted in probe.
 - The two types of noise are:
 - Output for noise on the outputs and
 - Input for noise on the input source.
 - Type of Sweep (same as for AC analysis)
 - Number of points... (same as for AC analysis)
 - Start Frequency (same as for AC analysis)
 - Stop Frequency (same as for AC analysis)
 - To use input noise you need to tell LTSpice where you consider the 'input' in your circuit to be, for example, if your voltage source is labelled 'V1'.
- F. DC Transfer

- HA	Edit Simulation Command	×
Transient AC Ana	alysis DC sweep Noise DC Transfer DC op pnt	1
	Find the DC small-signal transfer function.	
	Output:	
	Juice.	
Syntax: .tf V(<out>[,</out>	, <ref>]) <src></src></ref>	
Cancel	ОК	

- G. Other types of analysis There are other SPICE analyses possible. Eventually I might get them in here, including
 - parametric
 - fourier
 - sensitivity

V. Types of Sources

A. Voltage Sources A voltage source can be configured in many possible ways. Right clicking on one will bring up the "Independent Voltag Source" window. The options which show up in the window will change as the function selected changes.

 (none)

Independent Volt	-
Functions (none) PULSE(V1 V2 Tdelay Trise Tfall Ton Period Noycles) SINE(Voffset Vamp Freq Td Theta Phi Noycles)	DC Value DC value: Make this information visible on schematic: 🔽
EXP(V1 V2 Td1 Tau1 Td2 Tau2) SFFM(Volf Vamp Fear MDI Fsig)	Small signal AC analysis(.AC) AC Amplitude: 1
PWL (r1 v1 t2 v2) PWL FILE: Browse	AC Phase: 0 Make this information visible on schematic: 🔽
	Parasitic Properties Series Resistance[o Parallel Capacitance(F): Make this information visible on schematic: I⊄
Additional PW/L Points Make this information visible on schematic:	Cancel

• This is your basic direct current voltage source that simulates a simple battery and allows you to specify the voltage value. 2. SINE

Independent Volta	ge Source - V1
Functions C (none)	DC Value:
PULSE(V1 V2 Tdelay Trise Tfall Ton Period Noycles) SINE(Voffset Vamp Freq Td Theta Phi Noycles)	Make this information visible on schematic: $ abla$
EXP(V1 V2 Td1 Tau1 Td2 Tau2) SFFM(Voff Vamp Fcar MDI Fsig)	Small signal AC analysis(.AC) AC Amplitude: 1
C PwL(t1 v1 t2 v2)	AC Phase: 0 Make this information visible on schematic: 🔽
DC offset(V) 10 Amplitude(V) 1 Freq[H2] 60 Tdelay[s] 1 Phi(deg) 1 Ncgcles: 1	Parasitic Properties Series Resistance[o Parallel Capacitance[F]: Make this information visible on schematic: ♥
Additional PW/L Points Make this information visible on schematic: 🔽	Cancel

• A few things to note about the alternating current source. First, there are two possible analyses which can be done and so there are two sets of parameters.

For an ac analysis, the parameters are:

• AC Amplitude which is the peak value of the voltage.

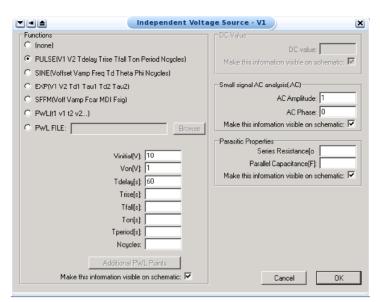
AC Phase which is the phase angle of the voltage

For a transient analysis, the parameters are:

- DC offset is the DC offset voltage. It should be set to zero if you need a pure sinusoid.
- Amplitude is the undamped amplitude of the sinusoid; i.e., the peak value measured from zero no DC offset value.
- Freq is the frequency in Hz of the sinusoid.
- \mathbf{T}_{delay} is the time delay in seconds. Set this to zero for the normal sinusoid.
- Theta is the damping factor. (Not the phase angle!) Also set this to zero for the normal sinusoid.
- This is used to apply an exponential decay to the sinusoid; theta is the decay constant in 1/seconds.
- **PHI** is the phase advance in degrees. Set this to 90 if you need a cosine wave form.
- N_{cycles} is the number of cycles of the pulse that should happen. Leave it as zero if you want ongoing pulses.

For this analysis, LTSpice takes it to be a sine source, so if you want to simulate a cosine wave you need to add (or subtract) a 90° phase shift. Note that the phase angle if left unspecified will be set by default to 0°

3. PULSE



- PULSE is often used for a transient simulation of a circuit where we want to make it act like a square wave source. It should never be used in a frequency response study because LTSpice assumes it is in the time domain, and therefore your probe plot will give you inaccurate results.
 - V_{initial} is the value when the pulse is not "on." So for a square wave, the value when the wave is 'low'. This can be zero or negative as required. For a pulsed current source, the units would be "amps" instead of "volts."
 - V_{on} is the value when the pulse is fully turned 'on'. This can also be zero or negative. (Obviously, V1 and V2 should not be equal.) Again, the units would be "amps" if this were a current pulse.
 - **T_{delay}** is the time delay. The default units are seconds. The time delay may be zero, but not negative.
 - **T**_{rise} is the rise time of the pulse. LTSpice allows this value to be zero, but zero rise time may cause convergence problems in some transient analysis simulations. The default units are seconds.
 - T_{fall} is the fall time in seconds of the pulse.
 - $\mathbf{T_{on}}$ is the pulse width. This is the time in seconds that the pulse is fully on.
 - T_{period} is the period and is the total time in seconds of the pulse.
 - N_{cycles} is the number of cycles of the pulse that should happen. Leave it as zero if you want ongoing pulses.
- This is a very important source for us because we do a lot of work with the square wave on the wave generator to see how various components and circuits respond to it.

Herein Independent Volt: Functions	
Functions	DC Value DC value: Make this information visible on schematic: Small signal AC analysis(AC) AC Amplitude: AC Phase;
C PWL FILE: Browse Vinitial[V]: 10 Vpulsed[V]: 1 Rise Delay[s]: 60 Rise Tau[s]: Fall Delay[s]: Fall Tau[s]: Fall Tau[s]:	Make this information visible on schematic: Parasitic Properties Series Resistance[o Parallel Capacitance[F] Make this information visible on schematic:
Additional PWL Points Make this information visible on schematic:	Cancel

- The EXP type of source is actually a
- Note that the normal usage of this source type is ...

5. SFFM

Independent Volt	age Source - V1
DC offset[V] DVLSE(V1 V2 Tdelay Trise Tfall Ton Period Noycles) SINE(Voffset Vamp Freq Td Theta Phi Noycles) EXP(V1 V2 Td1 Tau1 Td2 Tau2) SFFM(Voff Vamp Fcar MDI Fsig) PwL(t1 v1 t2 v2) DC offset[V] DC offset[V] Carrier Freq[H2] 60 Modulation Index: Signal Freq[H2]	DC Value DC value: Make this information visible on schematic: ✓ Small eignal AC analysis(AC) AC Amplitude: 1 AC Phase: 0 Make this information visible on schematic: ✓ Parasitic Properties Series Resistance(n Parallel Capacitance(F): Make this information visible on schematic: ✓
Additional Pw/L Points Make this information visible on schematic:	Cancel OK

The SFFM (Single Frequency FM) type of source is actually a
Note that the normal usage of this source type is
PWL (Piece-Wise Linear)

Independent Volt	age Source - V1
Functions © (none)	DC Value DC value:
PULSE(V1 V2 Tdelay Trise Tfall Ton Period Noycles) SINE(Voffset Vamp Freq Td Theta Phi Noycles)	Make this information visible on schematic:
EXP(V1 V2 Td1 Tau1 Td2 Tau2)	Small signal AC analysis(AC)
SFFM(Voff Vamp Fear MDI Fsig) PWL(t1 v1 t2 v2)	AC Amplitude: 10 AC Phase: 0 Make this information visible on schematic: 🔽
C PWL FILE: Browse	Parasitic Properties
time1[s]: value1[V]:	Series Resistance[o Parallel Capacitance[F]:
time2[s]:	Make this information visible on schematic: 🔽
time3(s):	
time4[s]:	
value4[v]: Additional PWL Points	
Make this information visible on schematic:	Cancel OK

- The PWL source is a Piece Wise Linear function that you can use to create a wave form consisting of straight line segments drawn by linear interpolation between points that you define. Since you can use as many points as you want, you can create a very complex wave form This source type can be a voltage source or a current source.
- The syntax for this source type is flexible and has several optional parameters. The required parameters are two-dimensional points consisting of a time value and a voltage (or current) value. There can be many of these data pairs, but the time values must be in ascending order, and the intervals between time values need not be regular.

7. PWL File

	Independent Volta	age Source - V1
Functions Functions PULSE(V1 V2 Tdelay Trise Tfall SINE(Voffset Vamp Freq Td Thel EXP(V1 V2 Td1 Tau1 Td2 Tau2) SFFM(Voff Vamp Fear MDI Fsig) PwL(t1 v1 t2 v2) FMUL FILE:	Ton Period Ncycles) ta Phi Ncycles)	DC Value DC value: Make this information visible on schematic: Image: Comparison of the schematic: Small signal AC analysis(AC) AC Amplitude: AC Amplitude: 10 AC Phase: 0 Make this information visible on schematic: Image: Comparison of the schematic: Parasitic Properties: Series Resistance[0 Parallel Capacitance[F]: Image: Comparison of the schematic: Make this information visible on schematic: Image: Comparison of the schematic:
Additiona Make this information vi	al PW/L Points	Cancel

• The PWL source is a Piece Wise Linear function that

B. Current Sources

- For any of the previous discussed voltage sources, there exist the exact source except that they produce current. There is one thing
 that should be mentioned; current sources in LTSpice get a little confusing. For those current sources whose circuit symbol has an
 arrow, you have to point the arrow in the direction of conventionally flowing current. This applies to all current sources, including AC
 and DC. Therefore placing the current source in the circuit backwards with seemingly incorrect polarities will give the correct results.
- An interesting little feature under the **markers** menu is the ability to add markers to your circuit so you can see where the current and voltage have imaginary values in the circuit, and the phase of your source at any point in the circuit.